

## John Levy, Ph.D. – Summary of technology areas in expert cases

Year	Client law firm or court	Nature of the case	Technology area	Specific areas & activities
2011	Connolly Bove Lodge & Hutz	Patent infringement	Disk drive interface	Encryption for ATA-interfaced disk drives
2011	Dorsey & Whitney	Patent infringement (ITC)	Storage	Encryption and access protection in flash drives
2010-11	Graves Dougherty Hearon & Moody	Patent infringement	User interface	Management of video playback
2010-11	Sprinkle IP Law Group	Patent infringement	Storage systems	Virtual local storage using disk subsystems
2010-11	Cooley	Patent infringement	Design automation	Circuit design emulation software
2010-11	Irell & Manella	Patent infringement	Hard disk design	Servo firmware for positioning of read/write heads
2010-11	Sughrue Mion; Bernstein Litowitz Berger & Grossman	Patent infringement	Software	Backup & recovery in PCs
2010-11	Sidley Austin	Patent infringement	PC design	BIOS code for power management
2009-10	Stroock & Stroock & Lavan	Patent infringement	Software	Operating system software for managing file & information retrieval & display
2009-10	DLA Piper	Class action – Employment law	Software	Classification of software engineers
2008-9	Bartlit Beck Herman	Patent infringement & trade secret	Hard disk design	Servo firmware for positioning of read/write heads
2008-9	DLA Piper	Patent infringement	Storage	Storage networking
2008-9	Morrison & Foerster	Patent infringement	Computer hardware	Cache design in multiple-bus systems
2008-9	Latham & Watkins	Patent infringement	Software	Embedded software for garage-door opener
2008-9	Ropes & Gray	Patent infringement	PC buses	Low-pin-count bus for PC
2008-9	Tingley Piontkowski	Patent infringement	Bus test equipment	USB bus testers
2008-9	Reeves & Brightwell	Class action	PC design	PC failures
2008-9	Sedgwick Detert Moran & Arnold	Trade secret	Software	Web design for commercial use
2008-9	Jones Day	Patent infringement	Computer hardware	Cache memory design
2008-9	Gowling Lafleur Henderson	Class action	PC design	Overheating and damage to motherboard of PC

2008-9	McDermott, Will & Emery	Patent infringement	Hard disk design	Zone recording & position on read/write heads in a hard disk
2006-7	Husch & Eppenberger, Kansas City	Contract dispute	Computer monitoring of power grid	Software customization & installation for real-time monitoring & reporting
2006-7	Morrison & Foerster, Palo Alto	Patent infringement	Audio/Video storage and playback	Digital video and audio stream storage and retrieval in consumer digital video recorder (DVR)
2005-6	Mintz, Levin, Cohn, Ferris Glovsky & Popeo	Patent infringement	Bus design for industrial control applications	Prior art search related to single-signal buses
2005-6	Fish & Richardson	Patent infringement	Multiprocessor cache coherency	Cache coherency in multiple-bus multiprocessor systems with shared memory
2005-6	DLA Piper Rudnick Gray Cary	Patent infringement	Hard disk design	Hard disk drive commands, ATA standards, creation of a "reserve area" on disk
2005	Meisel & Krentsa,	Personal injury	ASIC design engineering	Testimony regarding ASIC design engineer capabilities and activities
2005	Wilson, Sonsini, Goodrich & Rosati	SEC Wells Notice	Software industry practice	Software product development, customary beta site agreements
2005	Morgan, Lewis & Bockius	Class action	PC motherboard design	Hardware design, noise & failure analysis; testing of motherboards under heavy input/output load
2005	Cesari & McKenna	Patent infringement	Graphical programming & displays	Industrial application of graphical programming and object-oriented programming languages
2005	Merchant & Gould	Patent infringement	Computer hardware – bus design	Development of speed improvements for ATA/IDE disk interface, similar to Ultra-ATA
2005	NautaDutilh NV	Patent infringement (Europe)	Computer hardware – memory controller	Memory controller containing "read-around-write" feature - executing memory read accesses ahead of queued write accesses
2004-2005	Farella Braun & Martel	Breach of contract	Hard disk drive	Putative failure of EEPROM chips in hard disk drives due to low resistance between pins
2004	U.S. District Court, W. Dist. of Washington, Hon. Marsha Pechman	Patent infringement	Internet (http) protocol	Using "cookies" for load balancing among multiple processors supporting a web server; briefed the Court on technology and terms of art in Internet protocol
2004	Weil, Gotshal & Manges; Dewey Ballantine	Breach of contract, trade secret	Hard disk drive manufacture	Factory visit to document manufacturing processes
2003-2004	Squire, Sanders & Dempsey; Orrick, Herrington & Sutcliffe	Patent infringement	Computer hardware – memory controller	Memory controller containing "read-around-write" feature - executing memory read accesses ahead of queued write accesses
2003-2004	U.S. District Court, N Dist. of California, Hon. Ronald Whyte	Trade secret	Application software for typeset	Attended a preliminary hearing in order to assist the judge in getting the adversaries and their experts to agree on a format for exchanging source code.

2003-2004	U.S. District Court, Northern District of California, Hon. Ronald Whyte	Patent infringement	Computer hardware	Tutored the Court on technology and terms of art - ATA/ATAPI Standard and CD-ROM controller interfaces
2001	Kirkland & Ellis	Patent infringement	Computer hardware	High-speed memory bus design; research ed prior art, preliminary preparation of expert report
2000	Finnegan, Henderson, Farabow, Garrett & Dunner	Patent infringement (ITC)	Computer hardware	Bus arbitration methods, including round-robin priority management;
1988	Skjerven, Morrill, McPherson, Franklin & Friel	Software copyright infringement	Firmware in a CPU chip	Firmware / microcode which defines the instruction set in a single-chip microprocessor; extensive pre-trial preparation supporting defendant's arguments of limited expression
1986	Bogle & Gates	Breach of contract	Software - source code license	Meaning of the term "version" in software development; performed inspection of source code for two versions of the operating system
1978	Cesari & McKenna	Patent infringement	Computer hardware	Bus and input/output channel for hard disks, tapes and other peripherals